

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

- Q 3
1. (Original) A high speed video mixer circuit, comprising:
a reference circuit generating a bias voltage; and
a differential pair receiving as input signals a video signal and said bias voltage, said differential pair providing a first output video signal corresponding to said video signal, and a second output signal amplifying said video signal.
 2. (Original) A high speed video mixer circuit as in Claim 1, further comprising:
a second reference circuit for generating a second bias voltage; and
a bias circuit biasing said first output video signal about said second bias voltage.
 3. (Original) A high speed video mixer circuit as in Claim 1, further comprising a voltage clamp, said voltage clamp preventing an input transistor of said differential pair from going into saturation.
 4. (Currently Amended) A high speed video mixer circuit, comprising:
a reference circuit generating a bias voltage;
a differential pair receiving as input signals a video signal and said bias voltage,
said differential pair providing a first output video signal corresponding to said video
signal, and a second output signal amplifying said video signal; and
a voltage clamp preventing an input transistor of said differential pair from going
into saturation;
~~A high speed video mixer circuit as in Claim 3,~~ wherein said voltage clamp comprises a bipolar transistor having a base terminal receiving said bias voltage and having an emitter terminal coupled to a terminal of said input transistor, such that said

terminal of said input transistor is clamped at substantially one base-emitter voltage drop above said bias voltage.

5. (New) A high speed video mixer circuit, comprising:

an emitter-coupled amplifier receiving as input signals a first video signal and a bias voltage, said emitter-coupled amplifier providing a first output video signal corresponding to said first video signal, and a second output signal amplifying said first video signal; and

a blanking circuit receiving as input said first output video signal and a ground reference, said blanking circuit providing a blanking output signal dependent upon a voltage level of the first output video signal.

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6. (New) A high speed video mixer circuit as in claim 5, wherein:

said blanking circuit provides a blanking output signal without the input of an external mixing control signal.

7. (New) A high speed video mixer circuit as in claim 5, wherein:

said first video signal includes a single channel from multiple channels of video information, and wherein said single channel can include any of said multiple channels.

8. (New) A high speed video mixer circuit as in claim 5, further comprising:

an output stage receiving as input signals said second output signal, said blanking output signal, and a secondary video input signal, said output stage generating a final video output signal including a contribution from at least one of the second output signal and the secondary video input signal, the contribution dependent upon the blanking output signal.

9. (New) A high speed video mixer circuit as in claim 8, wherein:

said secondary video input signal includes information for a single channel from multiple channels of secondary video input, and wherein the contribution from the

secondary video input signal further includes information for each of said multiple channels.

10. (New) A high speed video mixer circuit as in claim 5, wherein:
said first video signal includes an on-screen display input video signal.

11. (New) A high speed video mixer circuit as in claim 8, wherein:
said secondary video input signal includes at least one of an analog video signal
and a digital video signal.

12. (New) A high speed video mixer circuit as in claim 5, further comprising:
a voltage clamp preventing an input transistor of said emitter-coupled amplifier
from going into saturation.
